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L8 and switch$ adj off	10

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Set Name side by side	Query	Hit Count	Set Name result set
	DB=PGPB,USPT,USOC,EPAB,JPAB,DWPI,TDBD; PLUR=YES; OP=OR		
L9	L8 and switch$ adj off	10	L9
L8	first adj microprocessor and second adj microprocessor and control adj system and fault	72	L8
L7	L6 and control adj system and fault	13	L7
L6	L4 and first adj microprocessor and second adj microprocessor	111	L6
L5	L4 and (miller-peter-john.in. or osborne-smith-andrew-charles.in. or lindsey-michael-john.in. or barnes-robert-john.in. or allen-david-john.in. ludar-smith-graham.in.)	2	L5
L4	700.clas.	51642	L4
L3	control adj system and load and first adj microprocessor and second adj microprocessor and fault and switched adj off	3	L3

DB=PGPB,USPT; PLUR=YES; OP=OR

L2 control adj system and load and first adj microprocessor and second adj
microprocessor and fault and switched adj off   2 L2

DB=PGPB; PLUR=YES; OP=OR

L1 control adj system and load and first adj microprocessor and second adj
microprocessor and fault and switched adj off.CLM.   1 L1

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Search Results -

Terms	Documents
L10 and first adj microprocessor and second adj microprocessor and control adj system and fault	2

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Search: L11

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L11	L10 and first adj microprocessor and second adj microprocessor and control adj system and fault	2	L11
L10	one adj side adj6 load	6388	L10
L9	L8 and switch$ adj off	10	L9
L8	first adj microprocessor and second adj microprocessor and control adj system and fault	72	L8
L7	L6 and control adj system and fault	13	L7
L6	L4 and first adj microprocessor and second adj microprocessor	111	L6
L5	L4 and (miller-peter-john.in. or osborne-smith-andrew-charles.in. or lindsey-michael-john.in. or barnes-robert-john.in. or allen-david-john.in. ludar-smith-graham.in.)	2	L5
L4	700.clas.	51642	L4

L3   control adj system and load and first adj microprocessor and second adj microprocessor and fault and switched adj off   3   L3

DB=PGPB,USPT; PLUR=YES; OP=OR

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L1   control adj system and load and first adj microprocessor and second adj microprocessor and fault and switched adj off.CLM.   1   L1

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control <phrase> system <and> fault <and>
switched <phrase> off <and> microprocessors
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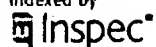» Learn more about Field Codes, Search Examples, and Search Operators

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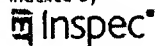
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<and> microprocessors
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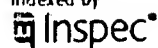**» Other Resources** (Availat

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microprocessors
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control <phrase> system <and> first
<phrase>  microprocessor
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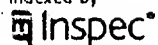- ○ Search latest content u
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☐   1. **Adaptive control system for a field-oriented induction motor drive**
Nen, C.; Schmitt, W.; Karakaxis, K.; Manias, S.N.;
Industrial Electronics, 1995. ISIE '95., Proceedings of the IEEE International S
Volume 1, 10-14 July 1995 Page(s):309 - 314 vol.1
Digital Object Identifier 10.1109/ISIE.1995.497014

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☐   2. **Universal protection and control applied to a 3-breaker transfer scheme**
Day, T.; McCall, J.; Gomes, D.;
Transmission and Distribution Conference, 1999 IEEE
Volume 2, 11-16 April 1999 Page(s):454 - 459 vol.2
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<phrase> microprocessor and fault
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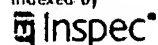» **Other Resources** (Availaı
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control <phrase> system <amd> (multiple  ▲
<or> dual <or> plurality) <phrase>
microprocessors
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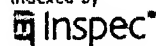
☐   1. **Fault tolerant parallel processing architectures for safety-critical applica**
       Thompson, H.A.;
       Parallel Processing: Industrial and Scientific Applications, IEE Colloquium on
       24 May 1991 Page(s):1/1 - 1/8
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control <phrase> system <and> (multiple
<or> dual <or> plurality) <phrase>
microprocessor
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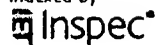» **Select date range**

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⬜ 1. **Development of microprocessors based demand control system for indu customer**
Ming Yuan Cho; Shih Wei Gau; Cha Win Huang;
Industrial and Commercial Power Systems Technical Conference, 2001. Conf Presented at the 2001 Annual Meeting. 2001 IEEE
15-16 May 2001 Page(s):1 - 6
Digital Object Identifier 10.1109/ICPS.2001.966504

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⬜ 2. **Effects of system architecture on safety and reliability of multiple micro| systems**
Milnor, R.C.; Washington, R.S.;
Vehicular Technology Conference, 1984. 34th IEEE
Volume 34, 21-23 May 1984 Page(s):306 - 315

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⬜ 3. **Automatic assembly for microelectronic components**
Zeungnam Bien; Sang-Rok Oh; Il-Hong Suh; Jae-Ok Kim; Young-Seok Oh;
Control Systems Magazine, IEEE
Volume 9, Issue 4, June 1989 Page(s):15 - 19
Digital Object Identifier 10.1109/37.24833

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⬜ 4. **Innovation to reality - introducing state-of-the-art protection and monitor voltage switchgear**
Reber, S.; Pintar, M.; Eaves, C.;
Cement Industry Technical Conference, 2004. IEEE-IAS/PCA
25-30 April 2004 Page(s):29 - 39

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5. **Engineering the CBTC interface with the NYCT R143 vehicle**
Schaefer, E.; Mortlock, E.;
Railroad Conference, 2001. Proceedings of the 2001 IEEE/ASME Joint
17-19 April 2001 Page(s):17 - 28
Digital Object Identifier 10.1109/RRCON.2001.921747

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6. **Fault tolerant parallel processing architectures for safety-critical applica**
Thompson, H.A.;
Parallel Processing: Industrial and Scientific Applications, IEE Colloquium on
24 May 1991 Page(s):1/1 - 1/8

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7. **Future research investment strategies**
Looft, D.J.;
Decision and Control including the 16th Symposium on Adaptive Processes a
on Fuzzy Set Theory and Applications, 1977 IEEE Conference on
Volume 16, Part 1, Dec. 1977 Page(s):1 - 1
Digital Object Identifier 10.1109/CDC.1977.271534

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